

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1 Attorney Docket Number PA2194US

Application Number	10/067,151
Filing Date	January 29, 2002
First Named Inventor	Charle' R. Rupp
Art Unit	2825
Examiner Name	Thuan V. Do
Attorney Docket Number	BA2124110

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Attorney Docket Number

PA2194US

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ *Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature	Murphy	Date Considered	7-6-04
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Transmittal is attached.
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IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Charle' Rupp

SERIAL NO.: 10/067,151

FILED: January 29, 2002

TITLE: Design and Methodology for Merging Programmable Logic
into Custom IC

EXAMINER: Do, Thuan V.

GROUP ART UNIT: 2825

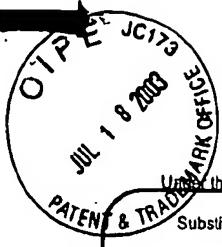
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List of Co-Pending Patent Applications That May Be Directed
Towards Similar Subject Matter

Examiner's Initials	Serial Number	First-Named Inventor	Title	Filing Date
TD	09/475,400	Rupp	Multi-Scale Programmable Array	12/30/99
TD	09/883,976	Rupp	Multi-Scale Programmable Array	06/19/01
Examiner's Signature:	<i>Murando</i>			Date 7-6-04



PTO/SB/08a (05-03)

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STATEMENT BY APPLICANT**

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Sheet

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of

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Complete if Known

Application Number	10/067,151
Filing Date	January 29, 2002
First Named Inventor	Rupp, Charle'
Art Unit	2825
Examiner Name	Do, Thuan V.

Attorney Docket Number PA2194US

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TP		US- 5,784,636	07-21-1998	Rupp	
TD		US- 2001/0049816 A1	12-06-2001	Rupp	
TD		US- 6,447,683 B1	11-05-2002	Killian et al.	
TD		US- 6,447,697 B1	11-05-2002	Killian et al.	
TD		US- 5,727,173	03-10-1998	Rupp	
TD		US- 6,282,633 B1	08-28-2001	Killian et al.	
TP		US- 6,426,648 B1	07-30-2002	Rupp	
TP		US- 6,292,388 B1	09-18-2001	Camarota	
TD		US- 6,418,045 B2	07-09-2002	Camarota	
		US-			

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Examiner Signature	<i>Murphy</i>	Date Considered	7-6-04
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